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Sheet 1 of 1

U.S. Department of Commerce, Patent and Trademark Office					Atty Docket No.		Serial No.	
					SP-2612-1C US		To be assigned	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT					Applicants			
(Use several sheets if necessary)					Tremblay, Marc; Joy, William			
					Filing Date		Group	
					Herewith		2783 2453	
U.S. Patent Documents								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
22	AA	5,530,817	6/25/96	Masubuchi	395	375		
↓	AB	5,657,291	8/12/97	Podlesny et al.	365	230.05		
	AC	5,721,868	2/24/98	Yung et al.	395	476		
	AD	5,761,475	6/2/98	Yung et al.	395	394		
	AE	5,764,943	6/9/98	Wechsler	395	394		
	AF	5,778,248	7/7/98	Leung	395	800.23		
	AG							
	AH							
	AI							
	AJ							
	AK							
Foreign Patent Documents								
							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
22	AL	0 213 843	11 Mar. 1987	Europe	G06F	9/30		
22	AM	0 272 150	22 Jun. 1988	Europe	G06F	9/42		
	AN							
	AO							
	AP							
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
22	AQ	"Technique for Reducing the Number of Registers Saved at a Context Swap" IBM Technical Disclosure Bulletin, vol. 33, no. 3A, August 1990, pages 234-235, XP000123918, US, IBM Corp. New York ISSN: 0018-8689						
↓	AR	"Technique to Improve Context Switching Performance in a CPU" IBM Technical Disclosure Bulletin, vol. 33, no. 3B, August 1990, pages 472-473, XP000124425, US, IBM Corp. New York ISSN: 0018-8689						
	AS	"Allocated Bits for Machines with Vector Registers" IBM Technical Disclosure Bulletin, vol. 33, no. 3A, August 1990, pages 310-314, XP000123953, US, IBM Corp. New York ISSN: 0018-8689						
Examiner		DAVID Y. ENG		Date Considered		7/15/02		
*EXAMINER: Initial in reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.								

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 13979 U.S. PRO